

Substitute for Form 1449 A & B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				<i>Complete if Known</i>	
				Application Number	09/917,682
Sheet 1 of 1				Confirmation Number	1169
				Filing Date	July 31, 2001
				First Named Inventor	Toru OHASHI
				Art Unit	2631
				Examiner Name	Not yet Assigned
				Attorney Docket Number	Q65604

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
UK		US 5,422,909		6/6/1995	Love et al.
UK		US 6,005,506		12/21/1999	Bazarjani et al.
UK		US 5,673,044		9/30/1997	Pellon
UK		US 4,185,325		1/22/1980	Appel
		US			
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁴
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁴
UK		Vladimir Friedman et al., "A Bit-Slice Architecture for Sigma-Delta Analog-to-Digital Converters", IEEE Journal on Selected Areas in Communications., vol. 6, no. 3, April 1988, pages 520-526, XP002221642, IEEE Inc. New York., US	

Examiner Signature	IKHAI TRAN	Date Considered	11/10/04
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